



Drafts

BRS:

Pending

Active

L1: (86) (PCI adj1 express

L2: (40) l1 and connector

L3: (5) (PCI adj1 express)

Failed

Saved

Favorites

Tagged (0)

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Queue

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DBs

USPAT

Default operator: OR

☒ Plurals☒ Highlight all hit terms initially

BRS form

ISR form

Image

Text

HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Err
1	BRS	L1	86	(PCI adj1 express) and port	USPAT	2006/11/06 09:55			
2	BRS	L2	40	l1 and connector	USPAT	2006/11/06 09:54			
3	BRS	L3	5	(PCI adj1 express) same port same connector	USPAT	2006/11/06 09:55			

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FileViewEditToolsWindowHelp

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OR

(PCI adj1 express) same port same connector

Plurals

Highlight all hit terms initially

BRS form

IS&R form

Image

Text

HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Re
1	<input type="checkbox"/>	<input type="checkbox"/>	US 7096310 B2	20060822	10	Switch configurable for a plurality of communication	710/317	710/315; 710/316	
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 7096308 B2	20060822		LPC transaction bridging across a PCI.sub.--express	710/315	710/306	
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 7088741 B2	20060808		Using an auxiliary channel for video monitor training	370/522	348/473; 370/236;	
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 7068686 B2	20060627		Method and apparatus for efficient transmission of	370/522	348/473; 370/236;	
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6992987 B2	20060131		Enumeration method for the link clock rate and the	370/252		

Start

EAST - [Un...